

ABSTRACT:

The invention relates to trimming of analogue filters (201) in integrated circuits by means of an automatic adjusting circuit. A local oscillator (202) in the automatic adjusting circuit provides a periodic reference signal (R) to an adjustable phase shifter (203), which on basis thereof, produces a periodic phase shifted signal (R\*). A phase detector (204) receives both the periodic reference signal (R) and the phase shifted period signal (R\*) and produces a test signal (T) in response to a phase difference between the periodic reference signal (R) and the periodic phase shifted signal (R\*). A lowpass filter (205) receives the test signal (T) and generates a level signal ( $T_{DC}$ ) relative a reference level, e.g. representing a zero voltage. A digital signal processor (207) produces a primary control signal ( $C_s$ ), having a serial format, on basis of the observation signal (M). A serial-to-parallel converter (208) converts the primary control signal ( $C_s$ ) into a control signal ( $C_p$ ) having a parallel signal format. The control signal ( $C_p$ ) influences a magnitude of at least one component value in the adjustable phase shift between the periodic reference signal (R) and the periodic phase shifted signal (R\*) attains a calibrated value being as close as possible to a desired value. A latch (210) forwards at least one signal element of the control signal ( $C_p$ ) for setting of at least one component value in the analogue filter (201) in accordance with a setting of at least one component value in the adjustable phase shifter (203) which produces the calibrated value.